

**Amendments to the Claims:**

This listing of claims will replace all prior versions, and listings, of claims in the application:

**Listing of Claims:**

Claim 1. (cancelled)

Claim 2 (original): A method of coupling a read/write head to a driver circuit having a current source, the method comprising the steps of:

operably coupling a read/write head to a flexible printed circuit, the flexible printed circuit having two opposing terminals for external coupling;

operably coupling a first impedance control circuit in parallel with the current source and a terminal of the flexible printed circuit; and

operably coupling a second impedance control circuit in parallel with the current source and the opposing terminal of the flexible printed circuit;

wherein the first impedance control circuit and the second impedance control circuit are approximately matched in impedance, and

a step of linking the opposing terminals with a capacitor for minimizing DC current loss.

Claim 3 (cancelled).

Claim 4 (currently amended): An impedance-controlled write driver circuit comprising:

a write head operably coupled to a flexible printed circuit, the flexible printed circuit having two opposing terminals for external coupling;

a symmetrical pair of matched impedance control circuits, each coupled between an opposing terminal of the flexible printed circuit and a write driver circuit ground;

two differential pair circuits, each coupled between the two opposing terminals; and

wherein ~~the~~ a head voltage  $V_h$  ~~may be~~ is described by the formula;

$V_h = I_W * (R_h + R_{fpc} + R_o) * (R_h + R_{fpc})$ , wherein;

$V_h = I_W * (R_h + R_{fpc}) * R_o / (R_o + R_h + R_{fpc})$ , wherein

$I_W$  represents a write head current;

$R_h$  represents a write head resistance;

$R_{fpc}$  represents the resistance of the flexible printed circuit; and

$R_o$  represents a selected internal reference resistance.

Claim 5 (currently amended): An impedance-controlled write driver circuit comprising:

a write head operably coupled to a flexible printed circuit, the flexible printed circuit having two opposing terminals for external coupling;

a symmetrical pair of matched impedance control circuits, each coupled between an opposing terminal of the flexible printed circuit and a write driver circuit ground;

two differential pair circuits, each coupled between the two opposing terminals; and

wherein the matched impedance control circuits each further comprise a resistor having a resistance value of about  $R_o/2$ , wherein  $R_o$  represents a selected internal reference resistance.

Claim 6 (original): An impedance-controlled write driver circuit comprising:

a write head operably coupled to a flexible printed circuit, the flexible printed circuit having two opposing terminals for external coupling;

a symmetrical pair of matched impedance control circuits, each coupled between an opposing terminal of the flexible printed circuit and a write driver circuit ground,

wherein the matched impedance control circuits each further comprise:

a first resistor having a resistance value of about  $R_o/2$  coupled to a first terminal of a capacitor, the capacitor having a second terminal coupled to ground;

a second resistor coupled to the second terminal of the capacitor in parallel with the first resistor, the second resistor having a resistance value of about  $R_{dc}/2$ ; wherein

$R_o$  represents a selected internal reference resistance; and

$R_{dc}$  represents a DC resistance.

Claim 7 (original): An impedance-controlled write driver circuit comprising:

a write head operably coupled to a flexible printed circuit, the flexible printed circuit having two opposing terminals for external coupling;

a symmetrical pair of matched impedance control circuits, each coupled between an opposing terminal of the flexible printed circuit and a write driver circuit ground; and

a first resistor, having a resistance value of about  $R_o$  in series with a capacitor, coupled between the two FPC terminals;

wherein the matched impedance control circuits each further comprise a second resistor having a resistance value of about  $R_{dc}/2$ ; wherein

$R_o$  represents a selected internal reference resistance; and

$R_{dc}$  represents a DC resistance of the impedance control circuit.

Claim 8 (original): An impedance-controlled write driver circuit comprising:

a write head operably coupled to a flexible printed circuit, the flexible printed circuit having two opposing terminals for external coupling;

a symmetrical pair of matched impedance control circuits, each coupled between an opposing terminal of the flexible printed circuit and a write driver circuit ground; and

a first resistor, having a resistance value of about  $R_o$  in series with a capacitor, coupled between the two FPC terminals, the path between the first resistor and capacitor further comprising a switch;

wherein the matched impedance control circuits each further comprise a second resistor having a resistance value of about  $R_{dc}/2$ ; wherein

$R_o$  represents a selected internal reference resistance; and

$R_{dc}$  represents a DC resistance of the impedance control circuit.

Claim 9 (original): An impedance-controlled write driver circuit comprising:

a write head operably coupled to a flexible printed circuit, the flexible printed circuit having two opposing terminals for external coupling;

a symmetrical pair of matched impedance control circuits, each coupled between an opposing terminal of the flexible printed circuit and a write driver circuit ground; and

a capacitor, coupled between the two flexible printed circuit terminals;

wherein the matched impedance control circuits each further comprise a resistor having a resistance value of about  $R_o/2$ , and having a first terminal coupled to a flexible printed circuit terminal, the resistor also having a second terminal coupled to the capacitor, wherein  $R_o$  represents a selected internal reference resistance; and

a diode coupled between the second terminal of the resistor and ground.

Claims 10 – 15 (cancelled).

Claim 16 (new): An impedance-controlled write driver circuit comprising:

a write head operably coupled to a flexible printed circuit, the flexible printed circuit having two opposing terminals for external coupling;

a symmetrical pair of matched impedance control circuits, each coupled between an opposing terminal of the flexible printed circuit and a write driver circuit ground; and

two differential pair circuits, each coupled between the two opposing terminals.